

PG-DVLSI: Verilog, UVM, HDL Simulation, System Architecture, CMOS

DVLSI

Post Graduate Diploma in
**VLSI Design
(PG-DVLSI)**

PG-DVLSI, a course derived from C-DAC's extensive research and development legacy, imparts knowledge in both theoretical and practical aspects of Very Large-Scale Integration design.

Course Modules

- Advanced Digital Design
- System Architecture
- Programming Fundamentals for Design and Verification
- Linux Shell Scripting & Python
- Verilog HDL
- HDL Simulation and Synthesis
- CMOS VLSI and Aspects of ASIC Design
- System Verilog
- Verification using UVM
- Aptitude & Effective Communication
- Project

Course Highlights

- Well established courses with excellent placement records.
- 24-week full-time courses with 900 hours' theory + lab + project.
- 6-8 hours per day theory + lab sessions on 6 days a week in most centres.
- Continuous lab and internal assessments during the course.
- Centralised course-end theory exams across all centres.
- Courses designed and developed in consultation with the domain experts in C-DAC, academia and ICT industry.
- Expert faculty from C-DAC and ICT industry with widespread domain knowledge.
- Tutorials, hands-on and projects relevant to the standards of the ICT industry.
- Special training on aptitude, effective communication and interview skills.



Scan for more details

प्रगत संगणन विकास केंद्र

CENTRE FOR DEVELOPMENT OF ADVANCED COMPUTING



www.cdac.in; acts.cdac.in



actssupport@cdac.in



+912025503134/107



www.facebook.com/officiallyACTS