

Category No.4: C-DAC, Bangalore, Project Engineer –VLSI Design (Physical Design)

Post:	Project Engineer – VLSI Design (Physical Design)
Post Code:	PE- 03/PD
No. of Posts	2
Duration of the position	Three Years
Educational Qualification	<ul style="list-style-type: none">• Full time 1st Class B.E/ B. Tech in Electronics & Communication / Telecommunication / Electronics or Equivalent with minimum 2 years of relevant post qualification work experience. <p style="text-align: center;">OR</p> <ul style="list-style-type: none">• Full time 1st Class B.E/ B. Tech in Electronics & Communication / Telecommunication / Electronics or Equivalent AND Full time 1st class M.E/M. Tech in Micro-Electronics/ VLSI Design/ Electronics or equivalent plus at least 1 year of relevant post qualification work experience.
Mandatory Experience (in one or more topics)	<ul style="list-style-type: none">• Experience in Physical Design handling Netlist to GDSII flow.• Experience in ASIC Synthesis, Floor Planning, Placement Optimizations, CTS, Routing and Timing Closure of Full-Chip designs.• Experience in physical verification (DRC/LVS/ERC) checks and reliability checks.• Hands-on-experience in Electronic CAD tools for layout design and physical verification.• Basic understanding of VLSI process, Electron Device Characteristics, IC Fabrication Technologies and IC packaging.• Good understanding of timing, power and area tradeoffs in design.
Other Skills and Qualities	<ul style="list-style-type: none">• Basic knowledge of scripting languages is desirable.• Good debugging, problem solving/ analytical skills.• Good interpersonal skills and ability to work in a team.
Mode of selection	Written Test and Interview
Monthly Consolidated salary	Rs. 31,000 /- plus based on the post qualification relevant experience
Maximum Age	30 years