

C-DAC, Bangalore, Project Engineer – VLSI Architecture / VLSI Circuit / VLSI System /
Microelectronic Circuit Design

Post:	Project Engineer – VLSI Architecture / VLSI Circuit / VLSI System / Microelectronic Circuit Design
Post Code:	PE-03/ACSMCD
No. of Posts	1
Duration of the position	Two Years – Extendable based on performance and availability of projects
Educational Qualification	<ul style="list-style-type: none"> • Full time 1st Class B.E/ B. Tech in Electronics & Communication / Telecommunication / Electronics or Equivalent AND • Full time 1st class M.E/M. Tech in Micro-Electronics/ VLSI Design/ Electronics or Equivalent AND • Full time PhD in the topics of VLSI Architecture / VLSI circuit design / VLSI System Design / Microelectronic Circuit Design. Candidates who have been awarded PhD degree during 2016 or later are ONLY eligible to apply. Candidates with PhD degree are ONLY eligible to apply.
Mode of selection	Written Test &/ Interview
Monthly Consolidated salary	Rs. 55,000 /- (minimum)
Maximum Age	37 years