

Category No.3: C-DAC, Bangalore, Project Engineer – VLSI Architecture / VLSI Circuit / VLSI System / Microelectronic Circuit Design

Post:	Project Engineer – VLSI Architecture / VLSI Circuit / VLSI System / Microelectronic Circuit Design
Post Code:	PE-03/ACSMCD
No. of Posts	1
Duration of the position	Two Years – Extendable based on performance and availability of projects
Educational Qualification	<ul style="list-style-type: none"><li>• Full time 1<sup>st</sup> Class B.E/ B. Tech in Electronics &amp; Communication / Telecommunication / Electronics or Equivalent</li></ul> <p style="text-align: center;"><b>AND</b></p> <ul style="list-style-type: none"><li>• Full time 1<sup>st</sup> class M.E/M. Tech in Micro-Electronics/ VLSI Design/ Electronics or Equivalent</li></ul> <p style="text-align: center;"><b>AND</b></p> <ul style="list-style-type: none"><li>• Full time PhD in the topics of VLSI Architecture / VLSI circuit design / VLSI System Design / Microelectronic Circuit Design. Candidates who have been awarded PhD degree during 2017 or later are ONLY eligible to apply.</li></ul>
Mode of selection	Written Test / Interview
Monthly Consolidated salary	Rs. 31,000 /- onwards based on domain expertise
Maximum Age	32 years