

Workshop on Analog VLSI & Mixed Signal Design

15-16 June 2017

CDAC Bangalore

Time slot	Speaker	Topic
Day 1 – 15th June		
9.30 – 10.00	Inauguration	
10.00 – 11.30	Dr. Ananda Mohan, CDAC Bangalore	Introduction to Mixed Signal Design – Analog filters
11.30 – 11.45	Tea Break	
11.45 – 1.15	Dr. Shouri Chatterjee, IIT Delhi	DC- DC Converters
1.15 – 2.00	Lunch	
2.00 – 3.30	Dr. Ananda Mohan, CDAC Bangalore	Analog filters continued – Sigma Delta ADC
3.30 – 3.45	Tea Break	
3.45 – 4.30	Mr. Venkata Reddy, CDAC Bangalore	Analog I/O Design
4.30 – 5.30	Dr. Sarat Chandra Babu, CDAC Bangalore	Analog fault Diagnosis
Day 2 – 16th June		
10.00 – 1.00	Dr. Bibhu Data Sahoo, Research Scientist at Univ. of Illinois at Urbana-Champaign	Analog to Digital Converters
1.00 – 2.00	Lunch	
2.00 – 3.30	Dr. K.R.K.Rao, TI, Bangalore	PLL Design
3.30 – 3.45	Tea Break	
3.45 – 5.00	Dr. Sampath Rao, Reva Institute of Technology, Bangalore	Analog Layout
5.00 – 5.30	Valedictory - Feedback	