



Two days Workshop on Analog VLSI & Mixed Signal Design

Date: June 15-16, 2017

Venue: C-DAC Knowledge Park, Bangalore-560038

Centre for Development of Advanced Computing (C-DAC), Bangalore is organizing two days workshop on Analog VLSI & Mixed Signal Design on 15-16 June, 2017. The two days workshop deals with the design of various building blocks for Analog VLSI. The following topics will be covered in the workshop. Fundamentals of filter design methodologies such as Active RC filters, OTA-C filters, Current-mode filters, Switched capacitor filters, Effect of non-ideal passive and active components such as tolerances of components, Finite gain, bandwidth of active devices, noise of passive and active devices, offset of active devices, parasitics due to interconnections as well as inherent parasitics, Recent trends in filter design for various mobile devices using GSM, CDMA, W-CDMA, Bluetooth technologies etc., will also be covered. Design of other analog building blocks such as A/D converters especially Sigma-delta converters of both discrete-time and continuous time types, PLL Design, Basics of RF CMOS design, Design of LNA - Mixers - Power amplifiers and oscillators, DC-DC Converters, Analog Fault diagnosis will also be covered.

SPEAKER PROFILE:

Dr. Ananda Mohan has obtained his Ph.D from Indian Institute of Science, Bangalore in 1975. He was with ITI limited since 1973 till 2003 in R&D. Later he was with Electronics corporation (ECIL) of India Limited as Executive director till 2008. After a one year stay at NTRO, at Bangalore, he was back with ECIL till 2014. Since April 2014, he is with C-DAC as Technology Advisor. He is a specialist in Analog filters and Cryptography.

Dr. K R K Rao was a professor at IIT Madras and is an outstanding mentor and teacher. He has taught numerous students at IIT Madras who have become experts in analog design across the world. He is at present with Texas Instruments Ltd, Bangalore, inspiring numerous young analog designers.

Dr. Sarat Chandra Babu has obtained his Ph.D. from IIT Delhi. He is a specialist in analog fault diagnosis. He has pioneered the Microprocessor Engineering program of Govt. of INDIA and has been spearheading several projects in IT security, e-governance, Internet of Things at C-DAC Bangalore.

Dr. Shouri Chatterjee received the B.Tech. degree in electrical engineering from the Indian Institute of Technology (IIT), Madras, India, in 2000, and the M.S. and Ph.D. degrees in electrical engineering from Columbia University, New York, NY, USA, in 2002 and 2005, respectively. From 2005 to 2006, he was a design engineer in the wireless division at Silicon Laboratories Inc., Somerset, NJ, USA. Since November 2006, he has been with the Faculty of the Department of Electrical Engineering of the IIT Delhi, India. His research interests include filter design, ultra-low-power circuits for energy scavenging,

and wireless front-ends for millimeter-wave radios.

Dr. Bibhu Data Sahoo received the B.Tech. from the IIT, Kharagpur, in 1998, the M.S. from the Univ. of Minnesota, in 2000, and the Ph.D. from the Univ. of California, Los Angeles in 2009. From 2000 to 2006, he was with DSP Microelectronics Group, Broadcom Corporation, Irvine, CA, and from Dec 2008 to Feb 2010, he was with Maxlinear Inc., Carlsbad, CA, where he was involved in design of ICs for signal processors and for CMOS TV tuners. From March 2010 to Nov 2010, he was a Post-Doc researcher with the Univ. of California, Los Angeles. From Dec 2010 to Dec 2011, he was an Asst. Prof. in IIT Kharagpur. From Jan 2012 to Dec 2015, he was an Assoc. Prof. in Amrita University, Amritapur. Since Jan 2016 he has been a Research Scientist at Univ. of Illinois at Urbana-Champaign. His research interests include analog and mixed signal circuits, data converters, and signal processing. He received the 2008 Analog Devices Outstanding Student Designer Award. He was the Assoc. Editor of IEEE Transactions on Circuits and Systems-II from Aug. 2014 to Dec. 2015.

Registration fees can be submitted through DD in favor of "C-DAC Bangalore", payable at Bangalore. NEFT details are given in the Registration link below.

Registration: <https://goo.gl/MEDXWb>

Category	Amount
Industry/Govt. R&D Labs	₹ 5000
Academia	₹ 3000
Students/Scholars	₹ 1500

For enquiry and clarification Contact

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Venue: C-DAC Knowledge Park, Bangalore
Address: Co-ordinator
 Workshop on Analog VLSI & Mixed Signal Design
 C-DAC Knowledge Park, No.1, Old Madras Road,
 Byappanahalli, Bangalore - 560038, India

Note:
 Participants need to take care of their transportation and accommodation during the training period. However, assistance can be provided upon request to help participants find nearby hotel / accommodation.