

Annexure-B

CORRIGENDUM dated 08/07/2019

(Tender No: CDACP/NSM-HPC-II/2019/263)

S.NO	Ref. in Tender	Clause in Tender	Corrigendum
	SECTION II	GENERAL CONDITIONS OF CONTRACT (GCC)	
1.	Page no.8, Point no:4.8	Eligibility Criteria: The bidder must have minimum annual sales turnover of Rs. 10 Crores for each of last three financial years.	The bidder must have minimum annual sales turnover of Rs. 4 Crores for each of last three financial years
	SECTION IV	PART A: 100TF HPC System based on Power processor @ C-DAC, Bangalore	
2.	Page no:19, Point no: A -1	HDD - Min. 2 x 2 TB SATA disk drive with Support for Hardware RAID 0, 1	HDD - Min. 2 x 2 TB SATA disk drive with Support for Hardware RAID 0, 1 or software RAID
3.	Page no:19, Point no: A -1 And Page no 19, Point no: A -2	Processor - 2 x IBM Power 9 Processor with min. 20-core and with min. 2.4Ghz frequency	Processor - 2 x IBM Power 9 Processor each with min. 20-core with NVLink and with min. 2.4Ghz frequency
4.	Page no 19, Point no: A -2	GPU: 4 x Nvidia Tesla V100 based NVlink	4 x Nvidia Tesla V100 connected over NVLink on SXM-2, CPU to GPU connectivity also on NVLink
5.	Page no: 19, Point no: A-2	Network - Two 1GbE network ports with PXE boot capability	Network - Two 1GbE network ports with PXE boot or equivalent capability for boot over network
	SECTION IV	PART B: PFS based storage for PART-A @ C-DAC, Bangalore	
6.	Page no: 22 and 23	Benchmark: 1. Bidder must submit storage benchmark results along with bid and should demonstrate 4 GB/s write throughput after installation.	Benchmark: 1. Bidder must submit storage benchmark or reference benchmark results along with bid and should demonstrate 4 GB/s write throughput after installation.

	SECTION IV	PART C :100TF HPC System based on AMD processor @ C-DAC, Pune.	
7.	Page no:24, Point no:2	Compute Nodes: 2 x AMD EPYC ROME processor with min. 32-core and with min. 2.5Ghz frequency having 16 DP flops / cycle for each core and native AVX2 support	2 x AMD EPYC ROME processor each with min. 32-core and with min. 2.5Ghz frequency having 16 DP flops / cycle for each core and native AVX2 support
8.	Page no. 24, Point no:2	compute nodes form factor: 1U rack mountable or smaller form factor.	2U rack mountable or smaller form factor
9.	Page no. 25, Point no:3	compute nodes with NVIDIA GPU: Processor : 2 x AMD EPYC ROME processor with min. 32-core and with min. 2.5Ghz frequency having 16 DP flops / cycle for each core and native AVX2 support	2 x AMD EPYC ROME processor each with min. 32-core and with min. 2.2Ghz frequency having 16 DP flops / cycle for each core and native AVX2 support
10. `	Page 27, Point no:6	Benchmarks: 1. Bidder must submit storage benchmark results along with bid and should demonstrate 4 GB/s write throughput after installation.	Benchmark: 1. Bidder must submit storage benchmark or reference benchmark results along with bid and should demonstrate 4 GB/s write throughput after installation.