

Category No.1: C-DAC, Bangalore, Project Engineer –VLSI Design

Post:	Project Engineer –VLSI Design
Post Code:	PE - 01/VLSI
No. of Posts	4
Location	C-DAC, Bangalore
Duration of the position	Three Years
Minimum Educational Qualification	Full time 1 st Class B.E/ B. Tech in Electronics & Communication / Telecommunication / Electronics (Candidates who have only B.E. / B. Tech degree are not eligible to apply). AND Full time 1 st class M.E/M. Tech in Micro-Electronics/ VLSI Design/ Electronics or equivalent. (Candidates who have passed out PG in the year 2017 or later are ONLY eligible to apply). A PG degree is MUST to apply for the post. Post-qualification experience is not mandatory.
Desirable Skills	<ul style="list-style-type: none">• Knowledge of RTL design, simulation, and modeling using VHDL/ Verilog/ System Verilog.• Knowledge of Digital IC Design (Simulation/ Synthesis [ASIC/ FPGA])• Fundamentals of Computer Architecture, Arithmetic Units Design, Bus protocols Architecture and Design, Memory Controllers Design.• Basic understanding of IC physical design, CMOS layout design, VLSI process, Electron Device Characteristics, IC Fabrication Technologies and IC packaging.• M. Tech Project thesis in the areas of RTL design / VLSI Design / VLSI architecture / ASIC Custom Design / Semi-custom ASIC Design / Analog Circuit Design related topics are preferred.
Other Skills and Qualities	<ul style="list-style-type: none">• Basic knowledge of C & Assembly level programming and scripting.• Good debugging, problem solving/ analytical skills.• Good interpersonal skills and ability to work in a team.
Mode of selection	Written Test and Interview
Monthly Consolidated salary	Rs. 31,000 /-
Maximum Age	30 years